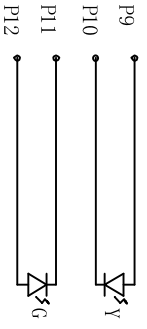
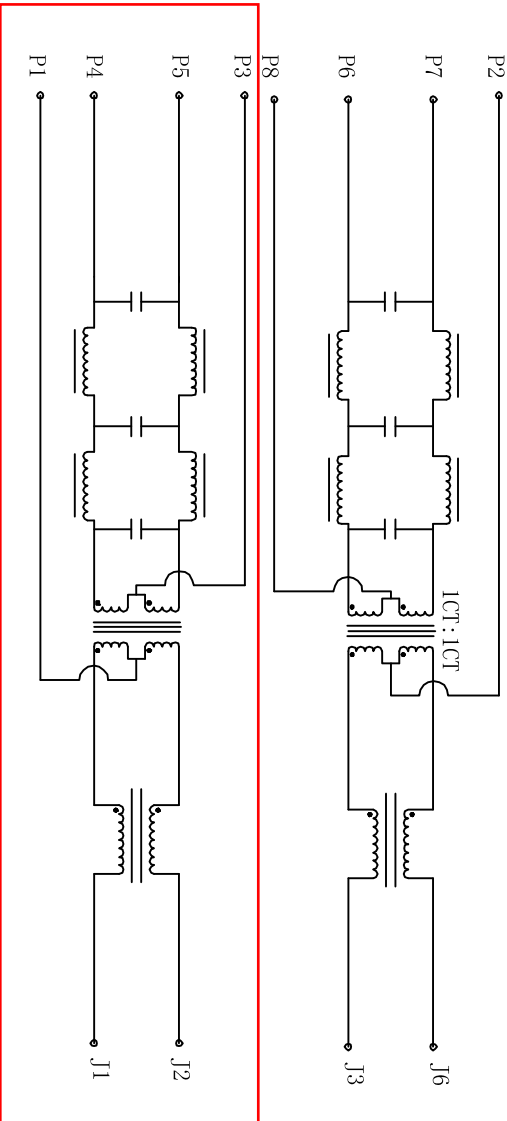


Rev	AWO #	Description	Date	Appr
		SEE SH1 OF 2 FOR REVISION		

APPROVED: \_\_\_\_\_

DATE: \_\_\_\_\_ TITLE: \_\_\_\_\_



**ELECTRICAL SPECIFICATIONS**

- Turns Ratio: (P4 ~ P5) : (J1 ~ J2) 1:1 ±3%  
(P6 ~ P7) : (J3 ~ J6) 1:1 ±3%
- Inductance: (P4 ~ P5) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias  
(P6 ~ P7) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
- Leakage Inductance: P4 ~ P5 (with J1 and J2 Short): 6.0uH Max. @ 1 MHz  
P6 ~ P7 (with J3 and J6 Short): 6.0uH Max. @ 1 MHz
- Interwinding Capacitor: (P4 ~ P5) : (J1 ~ J2): 35pF max. @ 1MHZ  
(P6 ~ P7) : (J3 ~ J6): 35pF max @ 1MHZ
- DC Resistance: (J1 ~ J2) : (J3 ~ J6): 1.2 Ohms Max.  
(P4 ~ P5) : (P6 ~ P7): 1.8 Ohms Max.
- Return Loss: 1MHZ to 30MHZ: -18dB min  
30MHZ to 60MHZ: -(18-20LOG(F/30))dB MIN (Where f is in MHZ)  
60MHZ to 80MHZ: -12dB min
- Dielectric Withstanding: (P4~P5) : (J1~J2) : 500V AC  
(P6~P7) : (J3~J6) : 500V AC
- Insertion Loss: RS=RL=1000HM  
1MHZ TO 10MHZ : -1.0dB Max.
- Cross Talk: 1MHZ to 100MHZ: -30dB min.  
10. Common to Common mode Attention 1MHZ to 100MHZ: -30dB min.

UNLESS OTHERWISE SPECIFIED  
ALL DIMENSIONS ARE INCHES [MM]  
TOLERANCES: EXCEPT AS NOTED

INCHES	HOLES
.X ± .10	Ø.X ± .10
.XX ± .020	Ø.XX ± .015
.XXX ± .015	Ø.XXX ± .010

APPROVALS

DRAWN	AY	5/22/12
CHECKED	SC	5/22/12
APPROVED		

**ADAM TECH**  
909 Ratway Avenue, Union, NJ 07083  
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TITLE	MODULAR JACK GANGED, 1 PORT, 8P8C
TITLE	FULL SHIELD, W/LED1 YELLOW, LED2 GREEN
SIZE	PART NO.
X	MTJ-887X1-FSD-D1LG-M168
REF: S0003C	SCALE: NTS
	SHEET 2 OF 2